

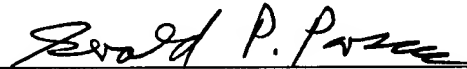
REMARKS

This continuation application is being filed to pursue claims to the use of a write cache memory in a flash EEprom (Electrically Erasable and Programmable Read-Only-Memory) system. Claims 63-67 being substituted for the original application claims are the same as those withdrawn from consideration in related application Serial No. 08/469,873, filed June 6, 1995, now abandoned. This write cache memory subject matter was also prosecuted in parent application Serial No. 07/337,566, filed April 13, 1989, now abandoned.

A prompt and favorable examination of this application is solicited.

Dated: September 16, 1997

Respectfully submitted,



Gerald P. Parsons, Reg. No. 24,486
MAJESTIC, PARSONS, SIEBERT & HSUE
Four Embarcadero Center, Suite 1100
San Francisco, CA 94111-4106
Telephone: (415) 248-5500
Facsimile: (415) 362-5418

Atty. Docket: HARI.006USG

08931133.091697